

LISTING OF THE CLAIMS

Please amend Claims 27, 40, and 43, as follows:

1-26. (Canceled)

27. (Currently amended) A flat panel display, comprising:

a first substrate;

a second substrate formed over a portion of the first substrate;

a plurality of gate lines and a plurality of data lines formed on the first substrate; and

a first gate driving signal transmission pattern formed on the first substrate and in electrical communication with the plurality of gate lines for transferring gate signals, wherein the first gate driving signal transmission pattern includes:

an input terminal being formed substantially adjacent to a data line group and being in electrical communication with a first external device;

an output terminal being formed substantially adjacent to a gate line group and being in electrical communication with a gate on/off voltage generating unit connected to the plurality of gate lines ~~a second external device~~; and

a main signal pattern arranged between and in electrical communication with the input terminal and the output terminal.

28. (Original) The flat panel display of claim 27, wherein the first substrate and the second substrate include a display region and a peripheral region arranged around the display region, and

wherein the first gate driving signal transmission pattern is formed in a portion of the peripheral region.

29-30. (Canceled)

31. (Previously presented) The flat panel display of claim 28, wherein the first external device generates a gate signal and a data signal and the first external device is arranged on a printed circuit board (PCB).

LAW OFFICES OF
MacPherson, Kneels, Chen &
Held LLP

2033 Gateway Place
Suite 400
San Jose, CA 95130
Telephone: (408) 392-8520
Facsimile: (408) 392-9262

32. (Original) The flat panel display of claim 31, further comprising a flexible film arranged on a portion of the PCB and a portion of the first substrate.

33. (Original) The flat panel display of claim 32, wherein the flexible film comprises a data driver integrated circuit (IC) which receives the data signal and generates a data driving signal.

34. (Previously presented) The flat panel display of claim 28, wherein the second external device includes a gate driver integrated circuit (IC) arranged on a second flexible film.

35. (Previously presented) The flat panel display of claim 34, wherein the gate driver integrated circuit (IC) generates a gate driving signal.

36. (Previously presented) The flat panel display of claim 28, further comprising a second gate driving signal transmission pattern formed on the first substrate.

37. (Original) The flat panel display of claim 36, wherein a second gate driving signal transmission pattern comprises:

a second input terminal;

a second output terminal; and

a second main signal pattern arranged between and in electrical communication with the second input terminal and the second output terminal.

LAW OFFICES OF
MacPherson, Kowal, Chen &
Heid LLP

2053 Gateway Place
Suite 400
San Jose, CA 95130
Telephone: (408) 392-9520
Facsimile: (408) 392-9502

38. (Original) The flat panel display of claim 37, wherein the second input terminal is in electrical communication with the first external device and a second gate driver integrated circuit (IC).

39. (Original) The flat panel display of claim 27, wherein the flat panel display is a liquid crystal display (LCD).

40. (Currently amended) A flat panel display, comprising:

a first substrate;

a second substrate arranged over the first substrate, wherein the first substrate and the second substrate include a display region and a peripheral region arranged around the display region;

a first signal transmission pattern formed in a peripheral region of the first substrate and in electrical communication with an external device, and including an input terminal formed substantially adjacent to a data line group and an output terminal formed substantially adjacent to a gate line group, wherein the first signal transmission pattern transmits a gate signal to a gate on/off voltage generating unit connected to the gate line group ~~a gate driver integrated circuit (IC);~~ and

a second signal transmission pattern formed along an edge portion of the first substrate, wherein the second signal transmission pattern transmits the gate signal to a second gate on/off voltage generating unit connected to the gate line group ~~a second gate driver integrated circuit (IC).~~

41. (Previously presented) The flat panel display of claim 40, wherein the first signal transmission pattern comprises:

an input terminal connected to a tape carrier package (TCP), wherein the tape carrier package (TCP) is arranged between the first substrate and the external device;

an output terminal connected to a second tape carrier package (TCP), wherein the second tape carrier package (TCP) includes the gate driver integrated circuit (IC); and

a main pattern formed between and in electrical communication with the input terminal and the output terminal.

42. (Previously presented) The flat panel display of claim 40, wherein the flat panel display is a liquid crystal display (LCD).

43. (Currently amended) A flat panel display, comprising:

a first substrate;

a second substrate arranged over the first substrate, wherein the first substrate includes a peripheral region arranged around a display region;

a tape carrier package (TCP) including a data driver integrated circuit (IC), wherein the tape carrier package (TCP) is arranged on a portion of a printed circuit board (PCB) and an external device on the PCB generates data signals and gate signals;

a first pattern formed in a peripheral region of the first substrate and in electrical communication with the external device, wherein the first line transmits the gate signals to a gate on/off voltage generating unit gate driver integrated circuit (IC) arranged on a second tape carrier package (TCP); and

a second pattern formed along an edge portion of the first substrate which transmits the gate signals to a second gate on/off voltage generating unit gate driver integrated circuit (IC).

44. (Previously Presented) The flat panel display of claim 43, wherein the first TCP includes a gate driving signal transmission pattern and data driving signal input pattern, wherein the data driving signal input pattern is electrically connected to the data driver integrated circuit (IC).

45. (Previously Presented) The flat panel display of claim 44, wherein the first TCP further includes a data driver signal output pattern electrically connected to the data driver integrated circuit (IC) and a data line group.

47. (Canceled).

LAW OFFICES OF
MacPherson, Kwoh, Chen &
Heid LLP

203 Gateway Place
Suite 400
San Jose, CA 95119
Telephone: (408) 392-4520
Facsimile: (408) 392-9202